

## 1.5-100MHz/160Watt/Module

Model Number: OC-PA00015-0.1K160W

The OC-PA00015-0.1K160W is suitable for multi octave broadband high power RF, MF&HF&VHF linear applications. This compact module utilizes advanced high power devices that provide excellent power density, high efficiency, wide dynamic range and low distortions. Exceptional performance, long term reliability and high efficiency are achieved by employing advanced broadband RF matching networks and combining techniques, EMI/RFI filters, machined housings and qualified components. Keylink's ISO9001 Quality Assurance Program assures consistent performance and the highest reliability.

### FEATURES:

- Small Size and light weight
- Instantaneous ultra broadband
- 50 Ohms input and Output matched
- Built-in control and protection circuits

### ELECTRICAL SPECIFICATIONS @ +28.0VDC, 25°C, 50Ω

Parameter	Symbol	Minimum	Typical	Maximum	Units
Operating Frequency	BW	1.5		100	MHz
RF Output Power	P <sub>out</sub>		160		Watt
Power Gain	G		52		dB
Small Gain Flatness	Δ G		±2		dB
Input Return Loss	S <sub>11</sub>			-10	dB
Harmonics @100W	H		-10		dBc
Spurious Signals	Spur		-60		dBc
In/Output Impedance			50		Ω
Operating Voltage	V <sub>DC</sub>	24	28	32	Volt
DC Current @100W	I <sub>DD</sub>		20		Amp

### MECHANICAL SPECIFICATIONS

Parameter	Value	Units	Limits
Dimensions	180x150x35[7x5.9x1.37]	mm [inch]	Maximum
Weight	2 [4.4]	kg [lbs]	Maximum
RF Connectors Input	SMA, Female		
RF Connectors Output	Type-N, Female		
DC Interface Connector	Hybrid,D-Sub 7-Pin,Male		
Cooling	External Heatsink (Not Supplied)		

### ENVIRONMENTAL CHARACTERISTICS (Design to Meet)

Parameter	Minimum	Typical	Maximum	Units	Notes
Operating Temperature	-20		60	°C	
Non-operating Temperature	-25		65	°C	Storage
Relative Humidity (non-condensing)			95	%	

### Absolute Maximum Rating

Input RF drive level without damage	+10 dBm	Maximum
Load VSWR @ P <sub>OUT</sub> =50W	∞ @ all load phase & amplitude for duration of 30 minute; 3:1 @ all load phase & amplitude continuous	
Thermal Overload	85°C shutdown	Maximum

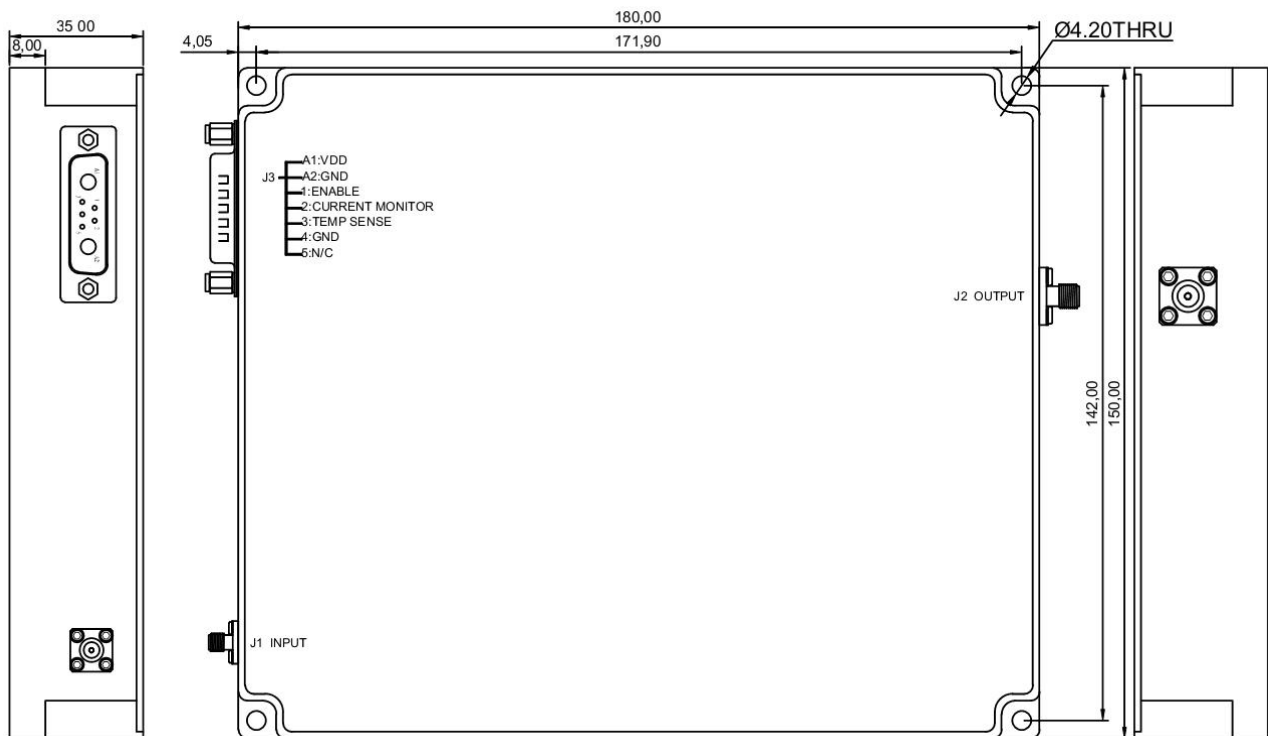
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#### DC INTERFACE CONNECTOR

Pin #	Description	Specifications
A1	VDD	28V <sub>DC</sub>
A2	GND	Ground
1	ENABLE	Amplifier Disable: TTL Logic High (3.3V) (Internally Pulled-Low)
2	CURRENT MONITOR	Analog voltage relative to I <sub>DD</sub> @ 100mV per Ampere
3	TEMP SENSE	Analog voltage relative to Module's Temperature @ 10 mV/°C
4	GND	Ground
5	NC	No electrical connection

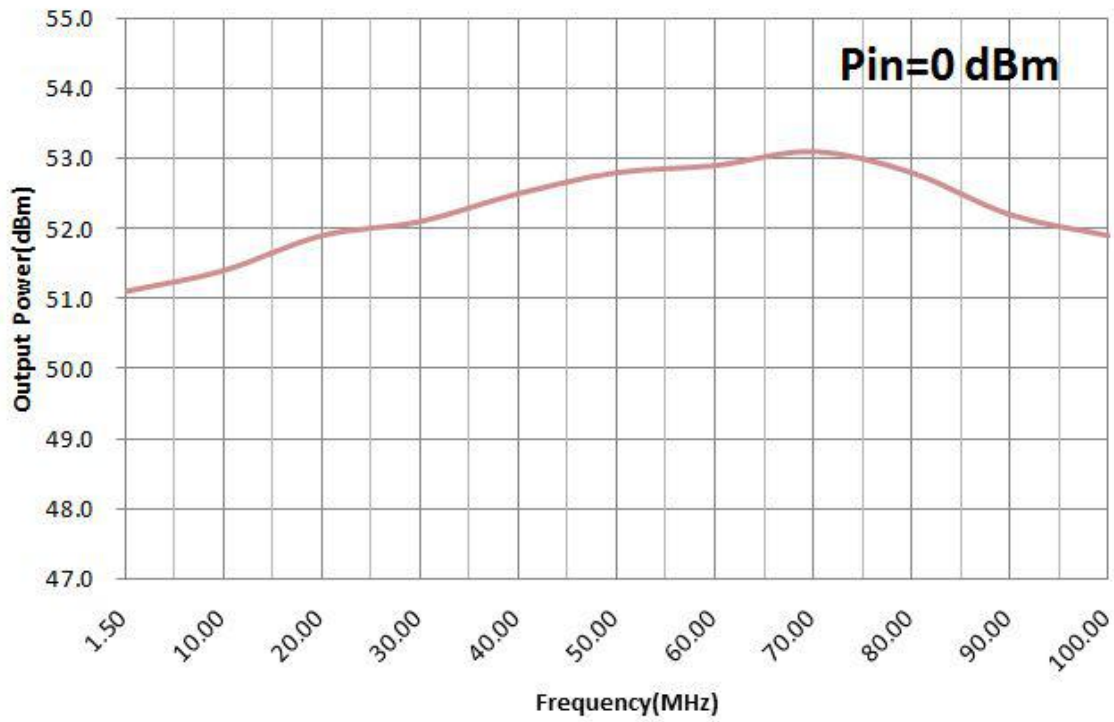
#### OUTLINE DRAWING (All dimensions in mm [inch])



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TYPICAL PERFORMANCE PLOTS



Note: Adequate heatsink required.