

GaAs monolithic integrated single pole double throw switch

DC~12GHz

key indicator

- \square Frequency range: DC \sim 12GHz
- Isolation: 48dB typ.
- ☐ Insertion loss: 0.8dBtyp.
- ☐ Control level: 0/+5V
- ☐ Nanosecond switch
- Chip size: 1.4mm×1.25mm×0.1mm

Product Introduction

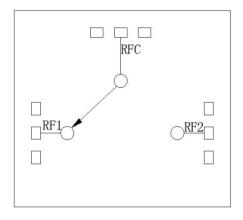
AY1767G is a broadband reflective gallium arsenide pHEMTSingle-pole double-throw switch chip, covering frequency range DC~12GHz, chipProvides typical 48dB isolation and0.8dB insertion loss. Use 0/+5V logic control. WorkingThe operating frequency band has excellent switching characteristics and port standing wave characteristics, suitable forSuitable for microwave hybrid integrated circuits and multi-chip modules and lowPower consumption system.

The switch chip uses an on-chip through-hole metallization process to ensure good qualityGood grounding. The back of the chip is metallized, which is suitable forCrystal sintering or conductive adhesive bonding process.

typical application

- ☐ Wireless communication equipment
- Radar and electronic countermeasures
- ☐ Military and aerospace
- Instruments and meters
- □ Test and measure

Functional block diagram



Electrical performance ($T_A=25^{\circ}$ C, control level=0/+5V, 50 Ω system)

index	Test frequency	Minimum	Typical value	Max	unit
Insertion loss	DC~12GHz	-	-0.8	-	dB
Isolation	DC~12GHz	-	-48	-	dB
VSWRRFC	DC~12GHz	-	1.2	-	
VAWRRF1, RF2(ON)	DC~12GHz	-	1.2	-	
Enter P ₋₁ dB	DC~12GHz	-	25	-	dBm
Enter IP 3	DC~12GHz	-	42	-	dBm
Switching time	DC~12GHz	-	30	-	ns

Absolute maximum ratings

RF input power	+27dBm	Operating temperature	-55 ℃ ~ + 85 ℃
Control voltage range	0~5.5V	Storage temperature	-65 ℃ ~ + 150 ℃
Electrostatic protection level (HBM)	Class1A	Channel temperature	150 ℃

Bias voltage & current

V _D	l _D	
-5V	2mA	

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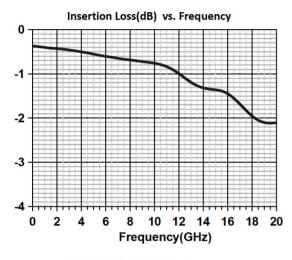
Control voltage

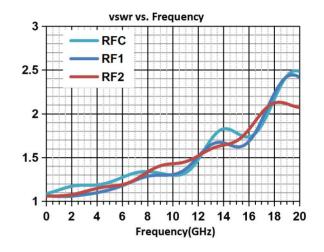
state	Bias condition		
Low	0~0.5V		
high	3.0~5.5V		

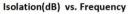
Truth table

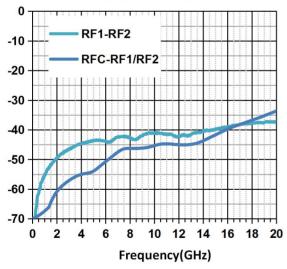
Control output	On-off state		
Ctr	RFC-RF	RFC-RF2	
Low	0	OFF	
high	OF	ON	

Typical test curve









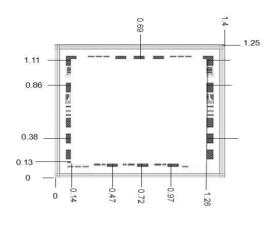


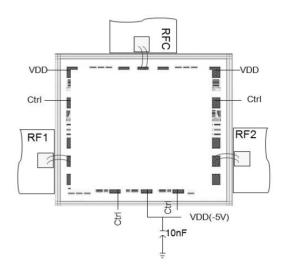
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Shape and port size (mm)

Recommended assembly drawing





Precautions

- 1. The chip is stored in a dry, nitrogen environment and used in an ultra-clean environment;
- 2. GaAs material is brittle and cannot touch the surface of the chip, so you must be careful when using it;
 3. Chips are sintered with conductive glue or alloy (the alloy temperature cannot exceed 300°C, and the time cannot exceed 30 seconds) to make it fully grounded;
- 4. The gap between the microwave port of the chip and the substrate should not exceed 0.05mm. Use Φ25μm double gold wire for bonding. The recommended length of gold wire is $250{\sim}400\mu m$;
- 5. The chip is sensitive to static electricity, so pay attention to anti-static during storage and use.